

# Notice of Allowability

Application No.

10/700,627

Examiner

MANSOUR M. SAID

Applicant(s)

YOO, TAE-KWON

Art Unit

2629

## -- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 12/29/06.
2. ☒ The allowed claim(s) is/are 1-11.
3. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) ☒ All b) ☐ Some\* c) ☐ None of the:
    1. ☒ Certified copies of the priority documents have been received.
    2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
    3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.
  - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached
    - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
  - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

### Attachment(s)

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☒ Information Disclosure Statements (PTO/SB/08),  
Paper No./Mail Date 11/5/03
4. ☐ Examiner's Comment Regarding Requirement for Deposit  
of Biological Material

5. ☐ Notice of Informal Patent Application
6. ☐ Interview Summary (PTO-413),  
Paper No./Mail Date \_\_\_\_\_
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other \_\_\_\_\_

  
**RICHARD HJERPE**  
SUPERVISORY PATENT EXAMINER  
TECHNOLOGY CENTER 2600

## DETAILED ACTION

### *Allowable Subject Matter*

**1. Claims 1-11 are allowed.**

The following is an examiner's statement of reasons for allowance: Claims 1-11 are allowed since certain key features of the claimed invention are not taught or fairly suggested by prior art. **In claims 1 and 6, “a detection unit for detecting in a predetermined region a maximum phase shift into digital format using the sampling clock signal; and a control unit for controlling the PLL (phase locked loop) circuit unit so that the sampling phase can be adjusted in accordance with the maximum phase shift detected by the detecting unit”.** The closest prior art Abe et al. (5,940,136) teaches dot clock reproducing comprising A/D converting means for receiving an adjusting signal delivered from a video signal source, and sampling this adjusting signal to convert into a digital signal, PLL means for dividing a specified synchronizing signal and generating a sampling clock for the A/D converting means, frequency analyzing means for analyzing the frequency of the adjusting signal from the output of the A/D converting means, Choi (6,404,422 B1) teaches an apparatus and a method for automatically controlling the screen status of a liquid crystal display, a microcomputer outputs control signals to compare first or second comparison data with reference data stored internal memory so as to detect the maximum value ... the sampling clock signals of the PLL circuit second are made to have the frequency and/or phase of the maximum value, however, singularly or in combination with other prior art, fail to anticipate or render the above underlined limitations obvious.

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2. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

### *Conclusion*

3. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Hwang (6,160,542) teaches a display includes a clock signal generator for generating a first sampling clock signal used for converting an analog video signal supplied from a host to a digital video signal.

Eglit et al. (6,459,426 B1) teaches a monolithic integrated circuit for use in a digital display unit. The circuit may include an analog-digital converter (ADC).

Lee (6,597,370 B1) teaches an apparatus for compensating a clock of a monitor.

Wang (6,856,358 B1) teaches a method to generate an optimum phase shifted sampling clock for sampling a synchronized video signal, having a synchronization signal pulse is achieved.

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Mansour M. Said whose telephone number is 571-272-7679. The examiner can normally be reached on Monday through Thursday from 8:30-6:00 P.M. The examiner can also be reached on alternate Friday from 8:30 a.m. to 5:00 p.m. EST. If attempts to

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reach the examiner by telephone are unsuccessful, the examiner's supervisor, Richard A. Hjerpe whose telephone number is 571-272-7681.

Any response to this action should be mailed to:

Commissioner of Patents and Trademarks

Washington, D.C. 20231


or faxed to: 571-273-8300 (for Technology Center 2600 only)

Hand-delivered responses should be brought to the Customer Service Window at the Randolph Building, 401, Dulany Street, Alexandria, VA 22314.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Mansour M. Said

2/12/07



**RICHARD HJERPE**  
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